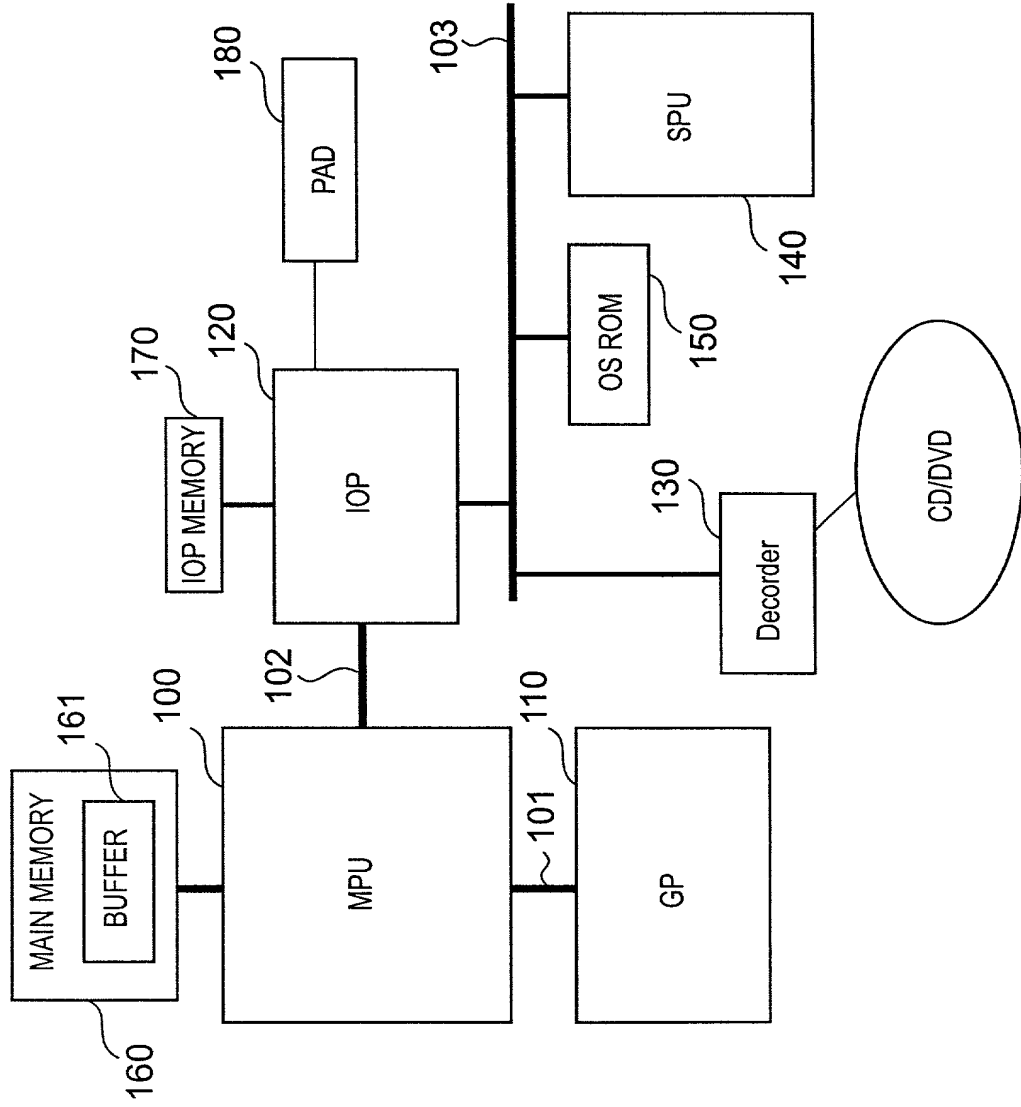


FIG.1



```

graph LR
    220((CD/DVD)) --> 200[READING PROCESSING PORTION]
    200 --> 161[BUFFER]
    161 --> 210[OUTPUT PROCESSING PORTION]
    210 --> 140[SPU]
  
```

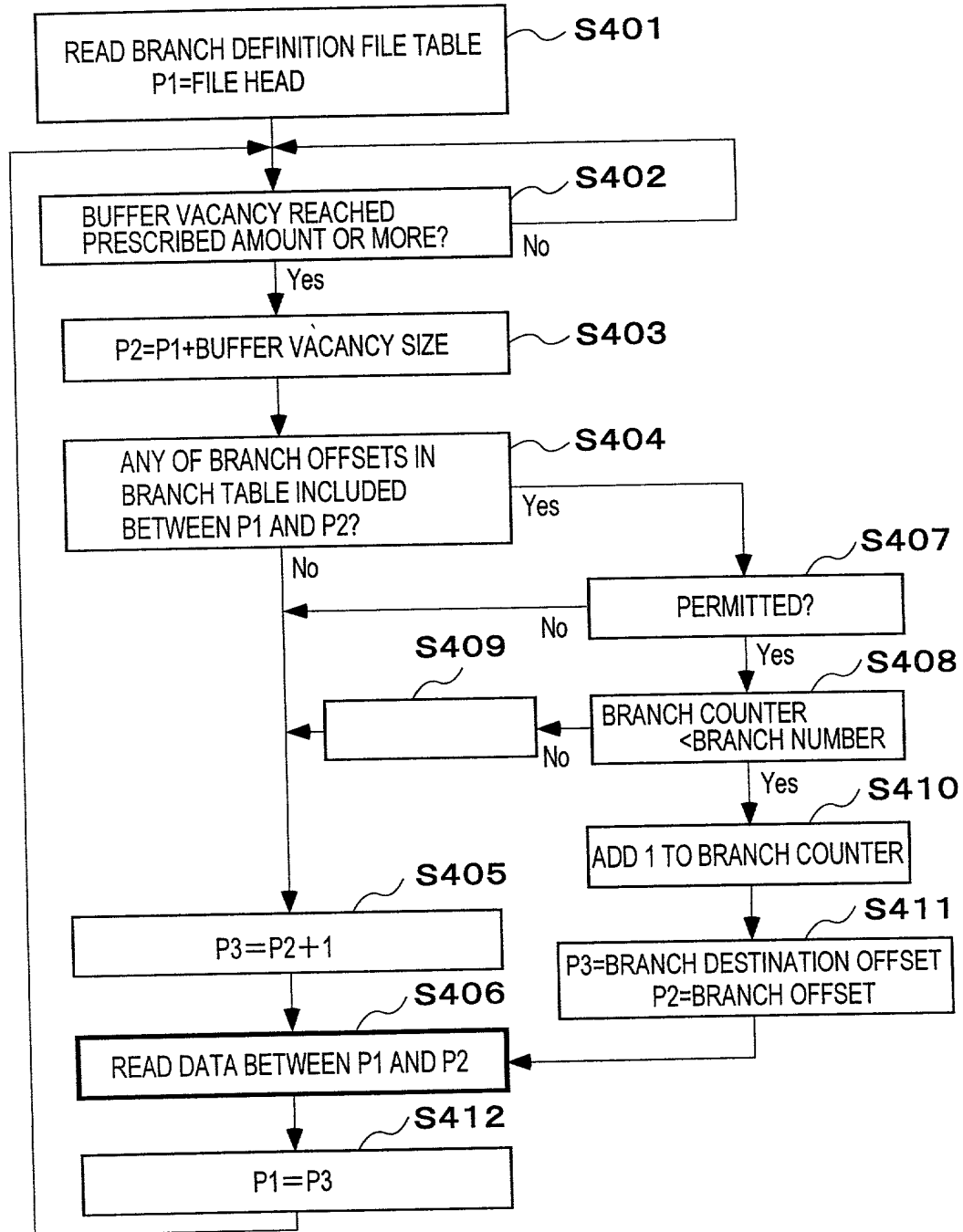
The diagram illustrates the system architecture. It consists of five main components connected in a linear sequence from left to right: a circular component labeled 'CD/DVD' (220), a rectangular component labeled 'READING PROCESSING PORTION' (200), a rectangular component labeled 'BUFFER' (161), a rectangular component labeled 'OUTPUT PROCESSING PORTION' (210), and a rectangular component labeled 'SPU' (140). Arrows indicate the flow of data from the CD/DVD through the reading processing portion, buffer, output processing portion, and finally to the SPU.

FIG.3

BRANCH DEFINITION FILE 300

	301	302	303	304	305
No.0	BRANCH POSITION OFFSET	BRANCH DESTINATION OFFSET	BRANCH COUNTER	BRANCH NUMBER	PERMISSION FLAG
No.1	BRANCH POSITION OFFSET	BRANCH DESTINATION OFFSET	BRANCH COUNTER	BRANCH NUMBER	PERMISSION FLAG
No.2	BRANCH POSITION OFFSET	BRANCH DESTINATION OFFSET	BRANCH COUNTER	BRANCH NUMBER	PERMISSION FLAG
No.3	BRANCH POSITION OFFSET	BRANCH DESTINATION OFFSET	BRANCH COUNTER	BRANCH NUMBER	PERMISSION FLAG
.....					
No.n	BRANCH POSITION OFFSET	BRANCH DESTINATION OFFSET	BRANCH COUNTER	BRANCH NUMBER	PERMISSION FLAG

FIG.4



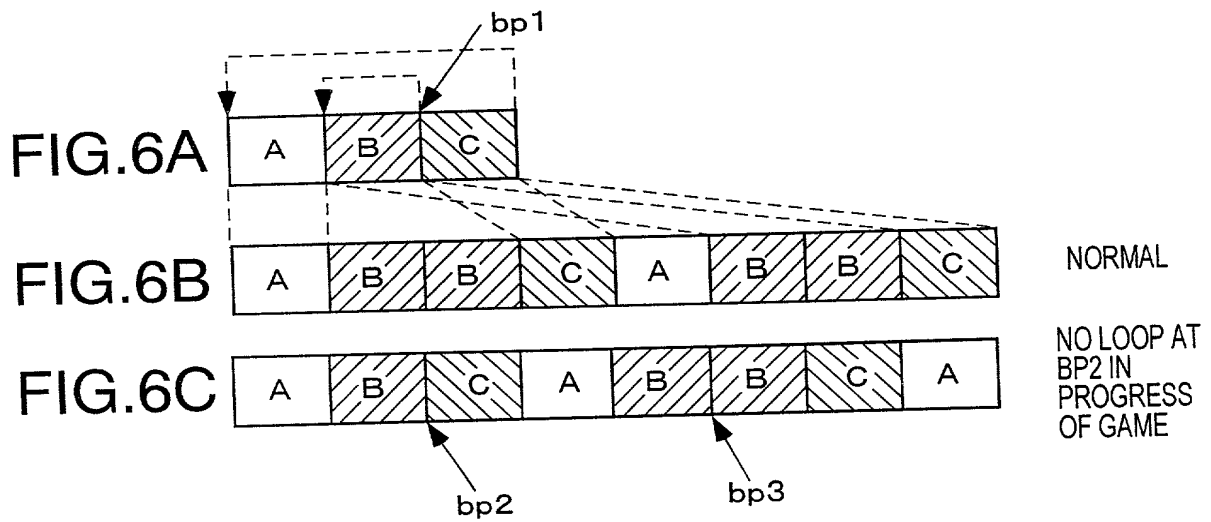
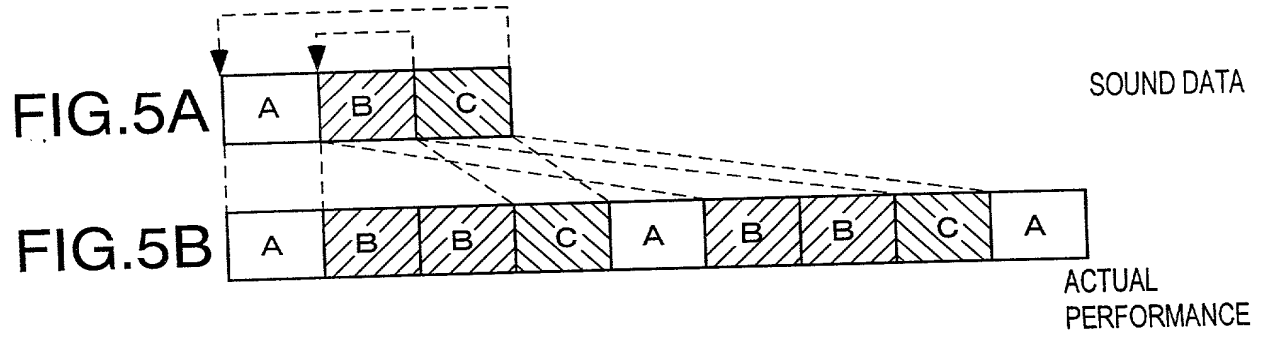


FIG.7

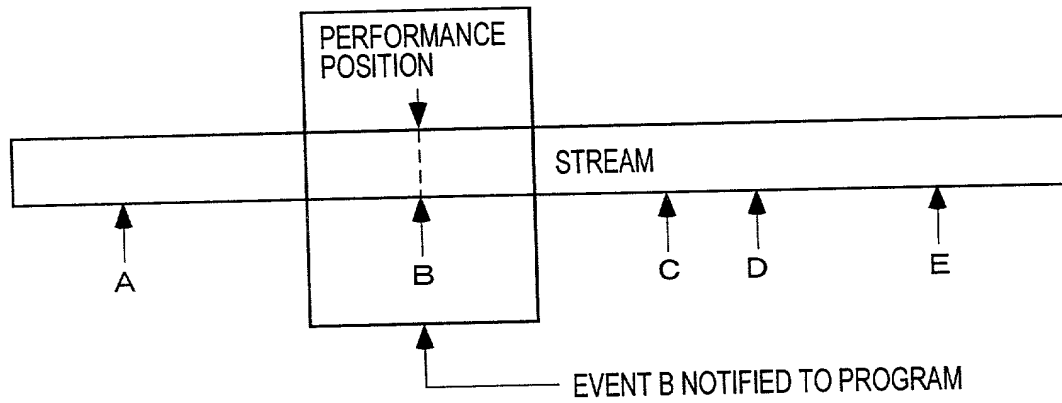


FIG.8

**ACTION DEFINITION FILE 800**

No.0	ACTION OCCURRENCE OFFSET	ACTION DATA
No.1	ACTION OCCURRENCE OFFSET	ACTION DATA
No.2	ACTION OCCURRENCE OFFSET	ACTION DATA
No.3	ACTION OCCURRENCE OFFSET	ACTION DATA
.....		
No.n	ACTION OCCURRENCE OFFSET	ACTION DATA

FIG.9

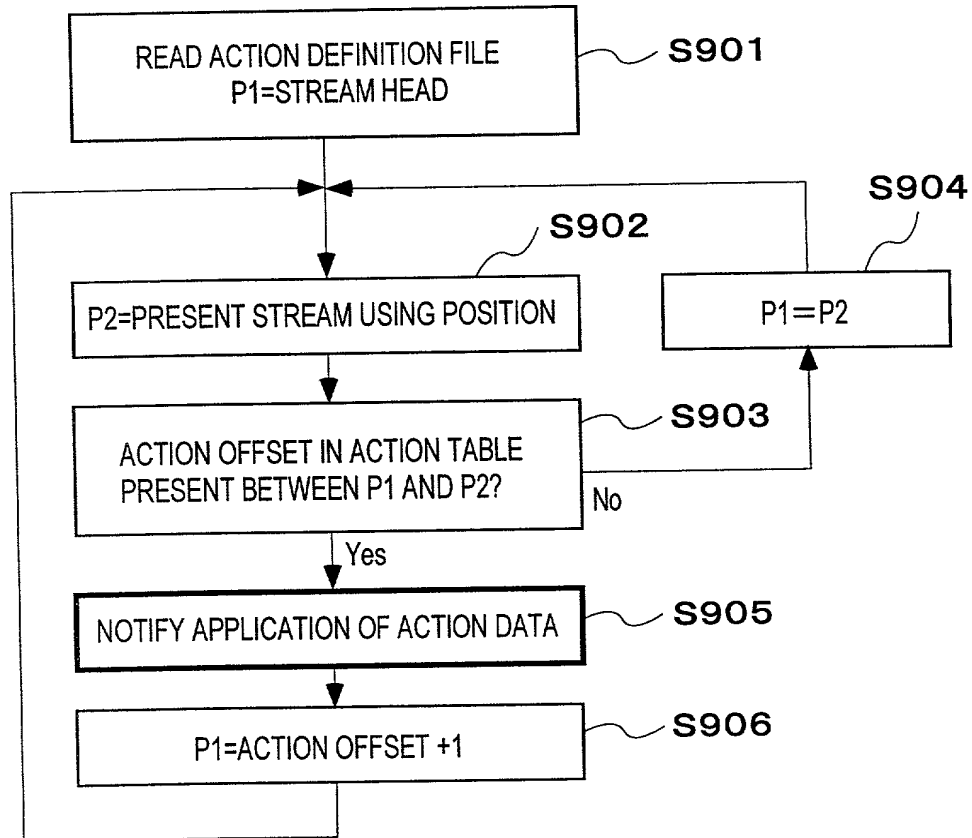


FIG.10A

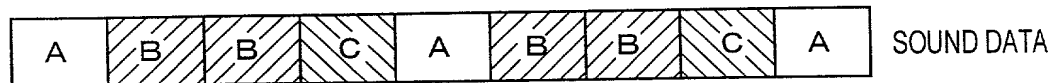


FIG.10B

